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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/750,342	12/29/2000	Sung-II Park	3430-0165P	6907

7590 07/28/2003

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EXAMINER

DUONG, THOI V

ART UNIT	PAPER NUMBER
	2871

DATE MAILED: 07/28/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action	Application No.	Applicant(s)
	09/750,342	PARK ET AL.
	Examiner	Art Unit
	Thoi V Duong	2871

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 09 July 2003 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

PERIOD FOR REPLY [check either a) or b)]

a) The period for reply expires 03 months from the mailing date of the final rejection.
 b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.
 ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. A Notice of Appeal was filed on _____. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.

2. The proposed amendment(s) will not be entered because:

- they raise new issues that would require further consideration and/or search (see NOTE below);
- they raise the issue of new matter (see Note below);
- they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
- they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: See Continuation Sheet.

3. Applicant's reply has overcome the following rejection(s): the objection of claims 5, 7, 8 and 10-13.

4. Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

5. The a) affidavit, b) exhibit, or c) request for reconsideration has been considered but does NOT place the application in condition for allowance because: _____.

6. The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.

7. For purposes of Appeal, the proposed amendment(s) a) will not be entered or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: 5-13.

Claim(s) objected to: _____.

Claim(s) rejected: 1-4 and 14.

Claim(s) withdrawn from consideration: _____.

8. The proposed drawing correction filed on _____ is a) approved or b) disapproved by the Examiner.

9. Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). _____

10. Other: _____


ROBERT H. KIM
 SUPERVISOR, EXAMINER
 TECHNOLOGY CENTER 2800

Continuation of 2. NOTE: The limitations "the source PCB and the gate PCB being formed along a first side and a second side, respectively, of the lower substrate ... wherein the first and second sides meet at a corner of the lower substrate" and "the source PCB is electrically connected with the gate PCB in the vicinity of the corner of the lower substrate" recited in claim 1, and "wherein the gate transmitting wires connect the gate pads to the source pads across the sealant in the vicinity of a corner of the first substrate" recited in claim 14 raise new issues that would require further consideration and/or search .